

### **REMARKS**

Applicants thank the Examiner for the thorough consideration given the present application. Claims 4 and 5 are currently being prosecuted. The Examiner is respectfully requested to reconsider his rejections in view of the amendments and remarks as set forth below.

#### *Rejection Under 35 U.S.C. §102*

Claims 1 and 3-5 stand rejected under 35 U.S.C. §102 as being anticipated by Nakamoto et al. (U.S. Patent No. 6,417,606). This rejection is respectfully traversed.

First, in regard to claims 1-3, these claims have been canceled rendering this part of the rejection moot. Claim 4 has been amended only to clarify one phrase which does not raise any new issues or insert any new matter.

The Examiner states that Nakamoto shows in FIGs. 1, 2, 7 and 8 a cathode substrate of a carbon nanotube field emission display having a glass substrate 701, a cathode layer 702 with the surface of the cathode layer having a plurality of electron emitting areas spaced apart from each other, and insulating layer 704 having an opening to expose the cathode layer and a gate electrode layer 705 formed over the top of the insulating layer 704.

Applicants submit that claim 4 is not anticipated by this reference. In particular, the Examiner points to layer 702 as the cathode layer. However, claim 4 describes the cathode layer as having a surface with a plurality of electron emitting areas spaced apart from each other and being uniformly arranged in an array. In Nakamoto et al., the layer 702 is a single layer and is not divided into a plurality of electron emitting areas which are spaced apart from each other. Instead, this layer is continuous to form a single layer between the borders of the insulating layer 704. Further, the areas are not uniformly arranged in an array as is presently

claimed. Such an arrangement is not disclosed in the patent and the Examiner has not indicated where such a limitation would be found in the patent. In view of this, Applicants submit that the Examiner has not met his burden of producing a complete anticipation rejection. If the Examiner persists in this rejection, he is requested to explain how the single layer 702 is considered to be a plurality of areas spaced apart from each other and uniformly arranged in an array. The Examiner is also requested to point out where this is found in the reference. For these reasons, Applicants submit that claim 4 is allowable.

Claim 5 depends from claim 4 and as such is also considered to be allowable. In addition, claim 5 describes the profile of the electron emitting area as quadrilateral, circular or some other physical appearance. Accordingly, Applicants submit that this claim is additionally allowable.

*Rejection Under 35 U.S.C. §103*

Claim 2 stands rejected under 35 U.S.C. §103 as being obvious over Nakamoto in view of Urayama et al. (U.S. Patent No. 6,650,061). This rejection is respectfully traversed. Since this claim has been canceled, Applicants submit that this rejection is rendered moot.

*Conclusion*

In view of the above remarks, it is believed that the claims clearly distinguish over the patent relied on by the Examiner. In view of this, reconsideration of the rejections and allowance of all the claims are respectfully requested.

Application No. 10/791,817  
Amendment dated November 4, 2005  
Reply to Office Action of August 5, 2005

Docket No.: 0941-0934P  
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If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees

Dated: November 4, 2005

Respectfully submitted,

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